

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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|-------------|---|---------------------------|--------------------|
| Applicant: | Vernon et al. | Art Unit: | 1756 |
| Serial No.: | 10/808,063 | Examiner: | Stephen D. Rosasco |
| Filed: | March 26, 2004 | Assignee: | Intel Corporation |
| | | Notice of Allowance Date: | June 4, 2007 |
| Title: | CONTINUOUS SLOPED PHASE EDGE ARCHITECTURE FABRICATION TECHNIQUE USING ELECTRON OR OPTICAL BEAM BLUR FOR SINGLE PHASE SHIFT MASK RET | | |

MAIL STOP ISSUE FEE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

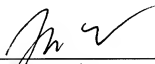
TRANSMITTAL OF FORMAL DRAWINGS

In response to the Notice of Allowability mailed June 4, 2007, please substitute the enclosed 4 sheets of formal drawings for the corresponding drawings presently in the application.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: September 4, 2007



Scott C. Harris
Reg. No. 32,030

Fish & Richardson P.C.
12390 El Camino Real
San Diego, California 92130
Telephone: (858) 678-5070
Facsimile: (858) 678-5099

BY
JOHN F. CONROY
REG. NO. 45,485